

REMARKS

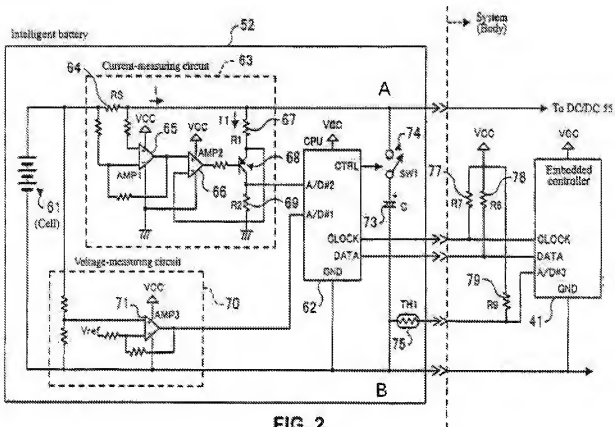
Claims 5-15 and 19-21 are withdrawn from consideration. Claims 1 and 4 stand rejected under 35 U.S.C. § 102(b) as anticipated by U.S. Patent Number 5,982,153 to Nagai (hereinafter Nagai). Claims 16, 23, and 26 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Nagai in view of United States Patent Application Number 2002/0026594 to Hayashi (hereinafter Hayashi). Claim 22 stands rejected under 35 U.S.C. § 103(a) as being unpatentable over Nagai.

Response to rejections of claims under 35 U.S.C. § 102(b).

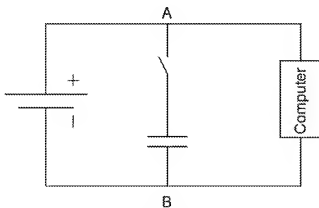
Claims 1 and 4 stand rejected under 35 U.S.C. § 102(b) as anticipated by Nagai. Applicant respectfully traverses the rejection.

Claim 1 includes the limitation of "...a switch in series combination with said high-capacity capacitor and said series combination of said switch and said high-capacity capacitor being coupled in parallel with said battery and said computer such that said battery, said computer, and said series combination share two common connections..." Claim 1.

Figure 2, shown below with two common connections labeled A and B, illustrates this limitation of claim 1. Figure 2, labels added. Figure 2 also shows where a computer is attached 55, a battery 61, and a series combination of a switch 74 and a high capacity capacitor 73. The series combination is coupled in parallel with the battery and computer. In addition, the series combination shares two common connections A and B with the battery and computer.

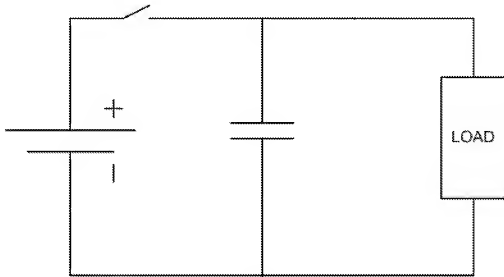


The basic elements of the limitation of claim I are redacted below, with the battery, computer and series of combination of switch and high capacity capacitor in parallel and sharing two common connections at A and B.



Present Invention

In contrast, Nagai teaches a battery 5 and switch SW1 series combination that is in parallel with a capacitor C2 and an inductive load L2. Nagai, fig. 2, ref. 5, SW1, C2, and L2. The capacitor is always in parallel with the battery and the load when the battery is connected to the load. Figure 2 of Nagai is shown redacted below.



Nagai

In Nagai, the series combination of the switch and the capacitor is not in parallel with the battery and the load and does not share two common connections with both the battery and the load. The Examiner asserts that the load, the battery, and the series combination are in parallel in Nagai. Office Action of February 9, 2007 (hereinafter OA070209), page 7, lines 2-4. However, as shown above, the series combination of Nagai is only in parallel with the battery. The series combination is not in parallel with the load.

The Examiner further asserts that “The switch and battery share the same connections as they are both connected to the ground.” OA070209, page 7, lines 4-5. However, in Nagai the

series combination, the battery, and the load only share one ground connection. The load does not share the 'A' connection with the battery and the series combination.

Because Nagai does not teach a series combination of a switch and a capacitor in parallel with a battery and a computer that shares two common connections with battery and computer, Applicant submits that claim 1 cannot be anticipated by Nagai and that claim 1 is allowable. Applicant further submits that claim 4 is allowable as depending from an allowable claim.

Response to rejections of claims under 35 U.S.C. § 103(a).

Claims 16, 23, and 26 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Nagai in view of Hayashi. Claim 22 stands rejected under 35 U.S.C. § 103(a) as being unpatentable over Nagai. Applicant respectfully traverses these rejections.

Claims 16, 22, and 23 include the limitation of a "...switch in series combination with said high-capacity capacitor and said series combination of said switch and said high-capacity capacitor being coupled in parallel with said cell and the computer such that said cell, the computer, and said series combination share two common connections..." Claim 16. See also claims 22 and 23.

As discussed above for claim 1, Nagai does not teach a series combination of a switch and a capacitor in parallel with a cell and a computer such that the cell, the computer, and the series combination share two common connections. Hayashi also does not disclose a series combination of a switch and a capacitor in parallel with a cell and a computer. See Hayashi, figs. 2, 7, 12, and 14. Because neither Nagai nor Hayashi teach a series combination of a switch and a

capacitor in parallel with a cell and a computer, Applicant submits that claims 16, 22, and 23 are allowable. Applicant further submits that claim 26 is allowable as depending from an allowable claim.

As a result of the presented remarks, Applicant asserts that claims 1, 4, 16, 22, 23, and 26 are in condition for prompt allowance. Should additional information be required regarding the traversal of the rejections of the claims enumerated above, Examiner is respectfully asked to notify Applicant of such need. If any impediments to the prompt allowance of the claims can be resolved by a telephone conversation, the Examiner is respectfully requested to contact the undersigned.

Respectfully submitted,

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